PATENT ABSTRACTS OF JAPAN

(11)Publication number:

10-308602

(43)Date of publication of application: 17.11.1998

(51)Int.CI.

H01P 1/15 H03K 17/76 H04B 1/44

(21)Application number: 09-114809

(71)Applicant:

FUJITSU LTD

(22)Date of filing:

02.05.1997

(72)Inventor:

TANAKA SADANORI

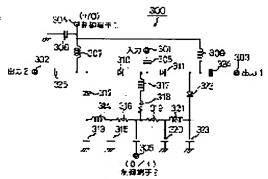
IWATSUKI HAJIME CHONO GAKUYO

(54) HIGH FREQUENCY SWITCH CIRCUIT

(57) Abstract:

PROBLEM TO BE SOLVED: To provide a high isolation and low insertion loss type circuit with an inexpensive pin diode by applying a switch control current based on the switch control potential of 2nd control terminal to the anode side of 1st diode and the cathode side of 2nd diode while separating that current from a high frequency signal.

SOLUTION: Impedance between the anode and cathode of forward biased pin diode 310 is turned into low state and the state between an input terminal 301 and an output terminal (2)302 is turned into conducted state. Besides, when a pin diode 322 is conducted, an output terminal (1)303 is grounded through a capacitor 323 in the manner of AC. On the other hand, both pin diodes 311 and 312 are turned off by backward bias. Then, a signal leaked to the side of output terminal (1)303 by the parasitic capacity of pin diode 311 increased in open state is biased through the pin diode 322 in conducted state to the ground, and the isolation between the output terminals (1)303 and (2)302 is improved.



LEGAL STATUS

[Date of request for examination]

02.03.2004

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office

BEST AVAILABLE COPY